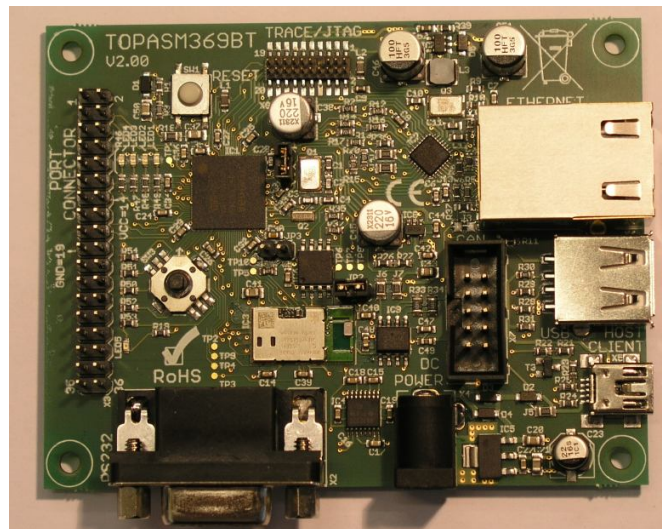


# BMSKTOPASM369BT(kc)

## Rapid Startup Guide



**Board Version: 2.00**  
**Manual Version: 02**

**Date: 27.01.2014**

By: Toshiba Electronics Europe GmbH  
European LSI Design Eng. Center – ELDEC

Microcontroller information:

<http://www.toshiba-components.com/microcontroller/index.html>

Wireless LSI information:

<http://www.toshiba-components.com/wireless/index.html>

## 1. Introduction

The "BMSKTOPASM369BT(kc) version 2" starter kit makes it possible to easily evaluate TOSHIBA Bluetooth® LSI "CHIRON", TC35661 and the Microcontroller TMPM369FDFG that offer the connectivity as Ethernet, CAN, USB host, USB device, UART, SIO, SPI and I2C.

The board uses the Panasonic (PIDEU) Bluetooth® module PAN1026 that has embedded the TC35661.

<http://pideu.panasonic.de/products/wireless-modules/bluetooth/bluetooth-40-dual-mode/pan1026-bluetooth-classic-and-low-energy-embedded-dual-mode-module.html>

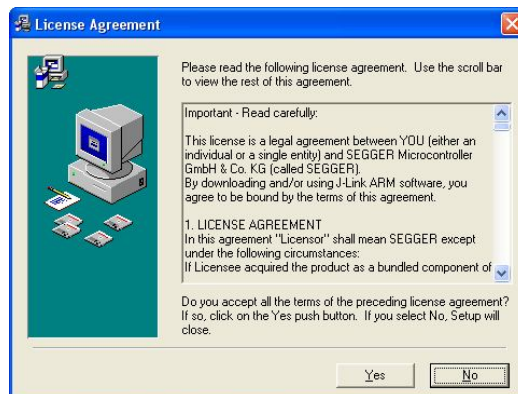
Starter-Kit Content:

- Toshiba TOPAS369BT Board
- Segger J-Link JTAG/SWD Emulator with USB interface including J-Link 19-pin Cortex-M Adapter and USB Cable

## 1. Step ONE: Driver Installation for Segger J-Link JTAG/SWD Emulator

! Before you connect the debug probe to the target board, please install the drivers first.

1. Before you plug your J-Link / J-Trace into your computer's USB port, extract the setup tool Setup\_JLinkARM\_V<VersionNumber>.zip. The setup wizard will install the software and documentation pack that also includes the certified JLink USB driver.



2. Start the setup by double clicking Setup\_JLinkARM\_V<Version-Number>.exe. More information about the installation and about the debug probe itself can be found on the [Segger website](http://www.segger.com).
3. Connect your J-Link via USB with your PC. The J-Link will be identified and after a short period the J-Link LED stops rapidly flashing and stays on permanently.

Link: <http://www.segger.com/cms/jlink.html>

## 2. Step TWO:

Depending on your preferences you may want to install and use one of the following IDEs with our board:

- Keil MDK™ with μVision® IDE/Debugger
- IAR® Kickstart Edition
- Atollic® TrueStudio® Lite

### Keil MDK™

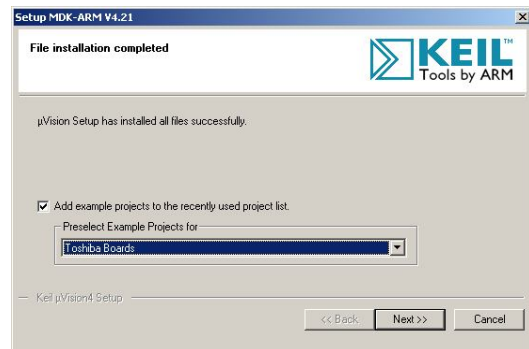
Software Tool Vendor :	Keil™, <a href="http://www.keil.com">www.keil.com</a>
Name of Environment :	MDK-ARM™, including Keil™ μVision®4 IDE/Debugger
Software Download Site :	<a href="https://www.keil.com/demo/eval/arm.htm">https://www.keil.com/demo/eval/arm.htm</a>

The Keil MDK™ μVision® software works together with the Segger J-Link and other tools. You can download the latest version from the software download site.

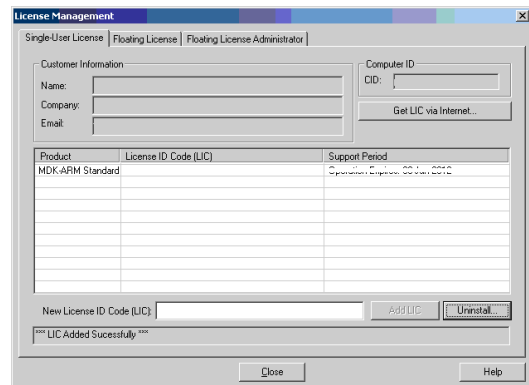
1. When you click on download you will have to register on the following form:
2. After registration you will be send to the download page, from where you can get the latest version of the software. Please download the file to your computer.

3. Start the executable file.

- At the end of the installation you will be asked for selecting the example projects. Select Toshiba Boards.



- If you have a registration code for ARM-MDK™ and want to install this, start up Keil MDK™. Select File → Licence Manager.
- Click on “Get LIC via Internet”
- On the opened Browser page fill in the code you have received under “Product Serial # (PSN)”
- After submitting you will receive a mail with the LIC.
- Copy this over to the “New Licence ID Code (LIC)” field and click “Add LIC”.

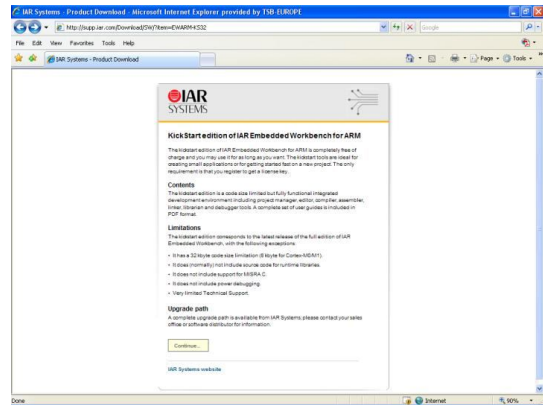


## **IAR EWARM®**

Software Tool Vendor :	IAR® , <a href="http://www.iar.com">www.iar.com</a>
Name of Environment :	Evaluation edition of IAR® Embedded Workbench for ARM
Software Download Site :	<a href="http://www.iar.com/Products/IAR-Embedded-Workbench/ARM/">http://www.iar.com/Products/IAR-Embedded-Workbench/ARM/</a>

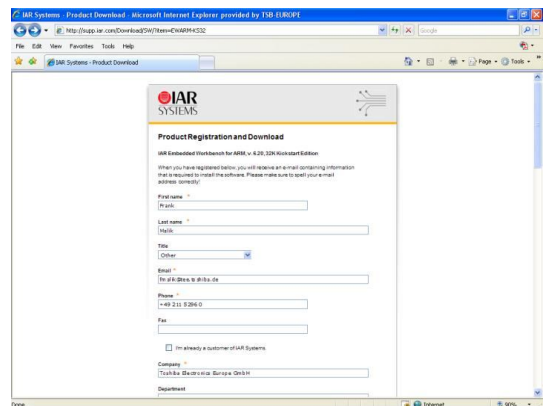
You can download the latest version from IAR® website under the link mentioned above. You have to register to download the Kickstart edition in example.

1. When you click on "continue" you will be send to the registration form.



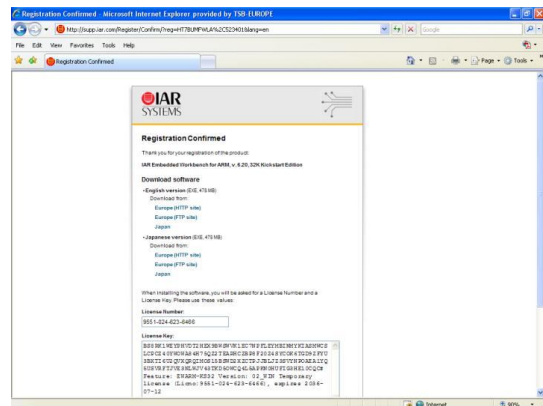
2. On the registration form you have to enter some basic information and answer some questions about your project.

3. After registration you will receive an email with further instruction.

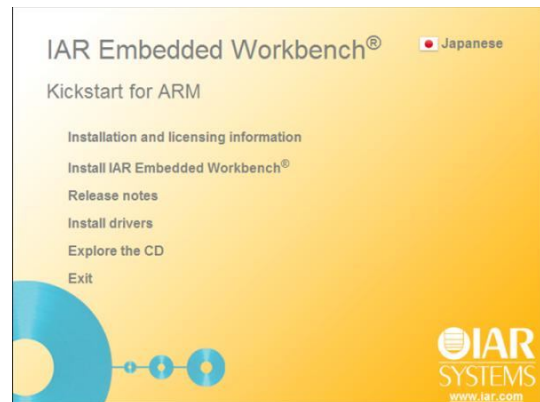


4. By confirming this registration you will open the download webpage which also contains your license number and license key. Optionally you can allow receive this information by email for later use. You should do this !

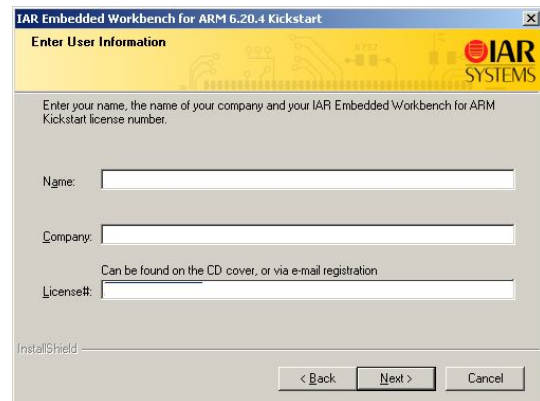
5. Download the executable from one of the listed websites.



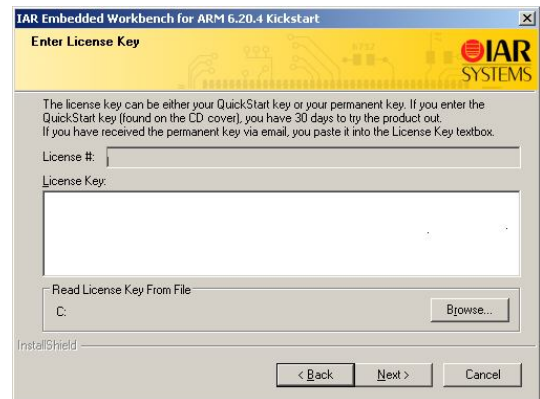
6. Start the downloaded executable file.
7. Click on "Install IAR Embedded Workbench"



8. Click on "Next" until you have to enter the User Information.
9. Please enter your Name and Company.
10. Enter or copy the License number from the Email you have received before.
11. Click "Next".



12. In the screen "Enter License Key", please copy the license key from the registration email.
13. Click "Next"
14. The installation will start now. Depending on the speed of your system this may take quite a while.

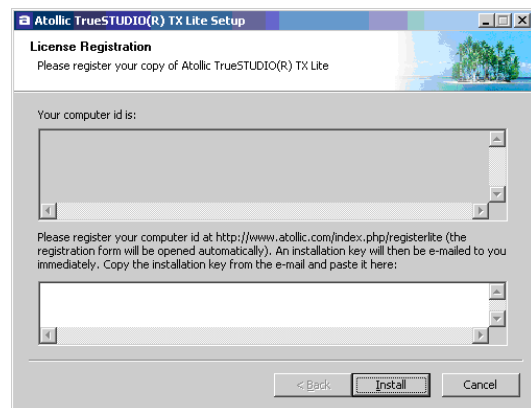


**Atollic® Truestudio®**

Software Tool Vendor :	Atollic® , <a href="http://www.atollic.com">www.atollic.com</a>
Name of Environment :	Atollic® TrueSTUDIO® Lite
Software Download Site :	<a href="http://www.atollic.com/index.php/download">http://www.atollic.com/index.php/download</a>

The Atollic® software works together with the Segger J-Link and other tools. You can download the latest version from the software download site.

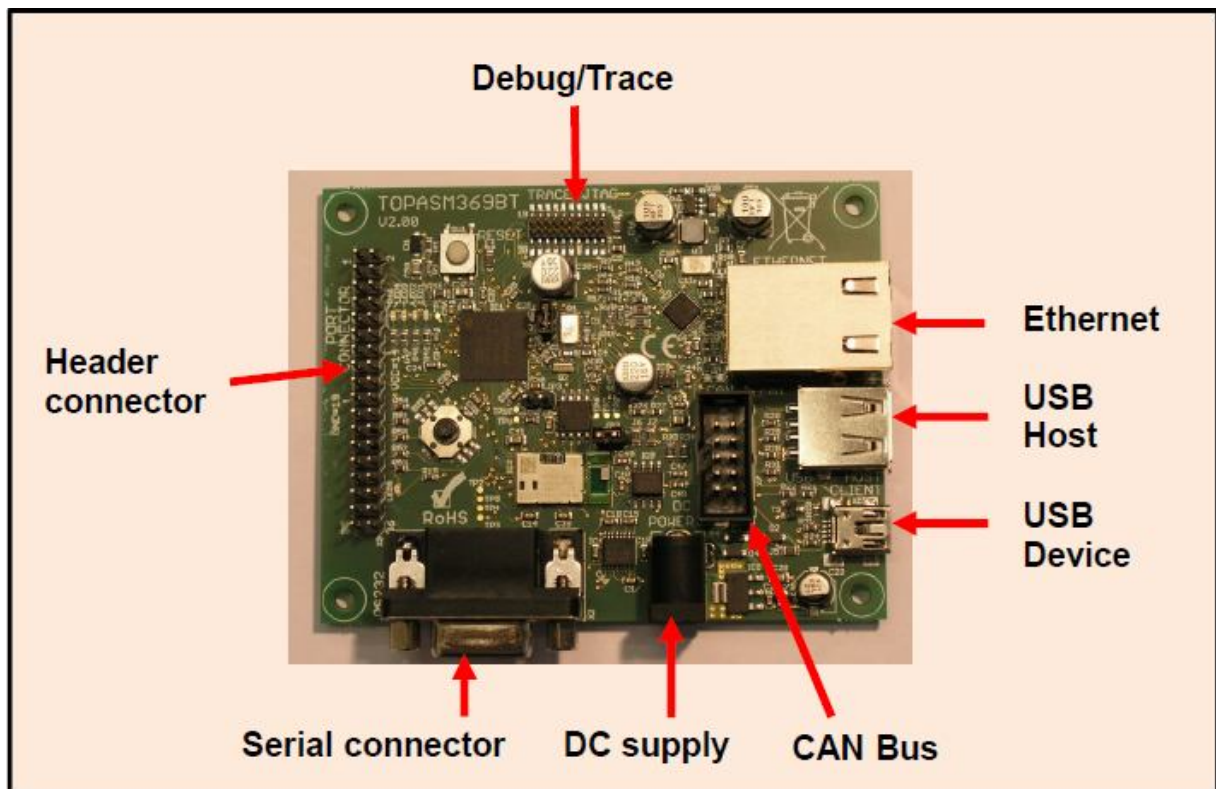
1. Start the installer.  
You will see the following screen popping up with your computer ID. You will require this information for the registration process. You will receive a license key that you have to enter into the second text area of this screen.
2. Go to the Atollic® website at <http://www.atollic.com/index.php/registerlite> to continue your registration process.
3. Fill out all registration data and click on register.
4. You will receive a mail with the installation key.
5. Copy this key and place it in the installation key box at the bottom of the installer window.
6. Just press the install button and wait until installation has finished.





### 3. Step THREE: Hardware Setup of the M369BT STK MCU Board

Place the MCU Board in front of you as below. The major connectors on the board are as follows.



The board can be powered in three ways:

- By the USB client connector
- By the debug connector
- By the DC jack connector

These three inputs are wired OR by diodes. There is no need to select a power input by a jumper.

!!! Please note when you make use of the power supply by a debug probe like the JLINK that comes with that starter kit, that this function is disabled by default. This can be enabled by the **J-Link Commander**

(JLink.exe) is a command line based utility that is part of the Segger J-Link software package. When you enter at the prompt the command "power on perm" power supply to the target will be done by the J-LINK.

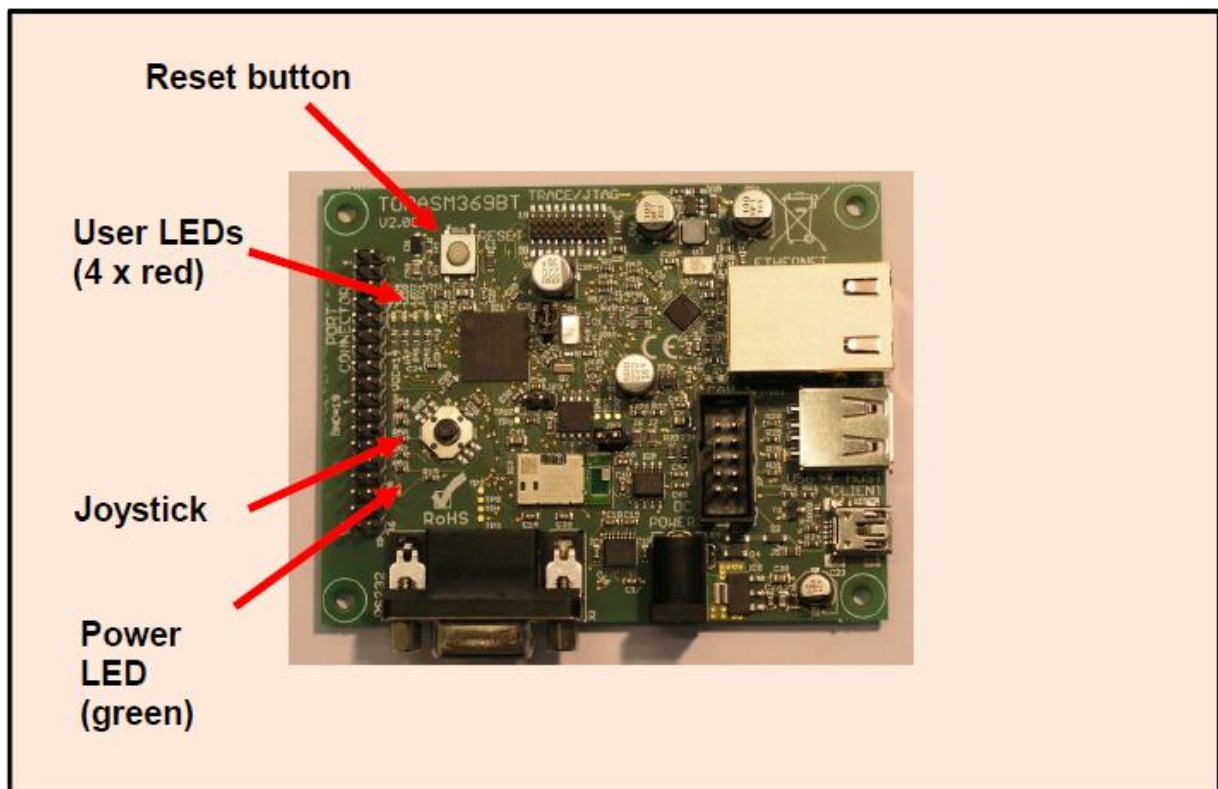
<http://www.segger.com/j-link-commander.html>

The main voltage of 3.3 volt is derived by a linear regulator. Hence DC jack input voltage must be in a range from 5...12 volts.

The USB host supply voltage of 5 volts is generated by a step-up converter from the 3.3 V bus. Please note that the current capability of this USB host is much lower than the USB specification of 500mA (~100mA).

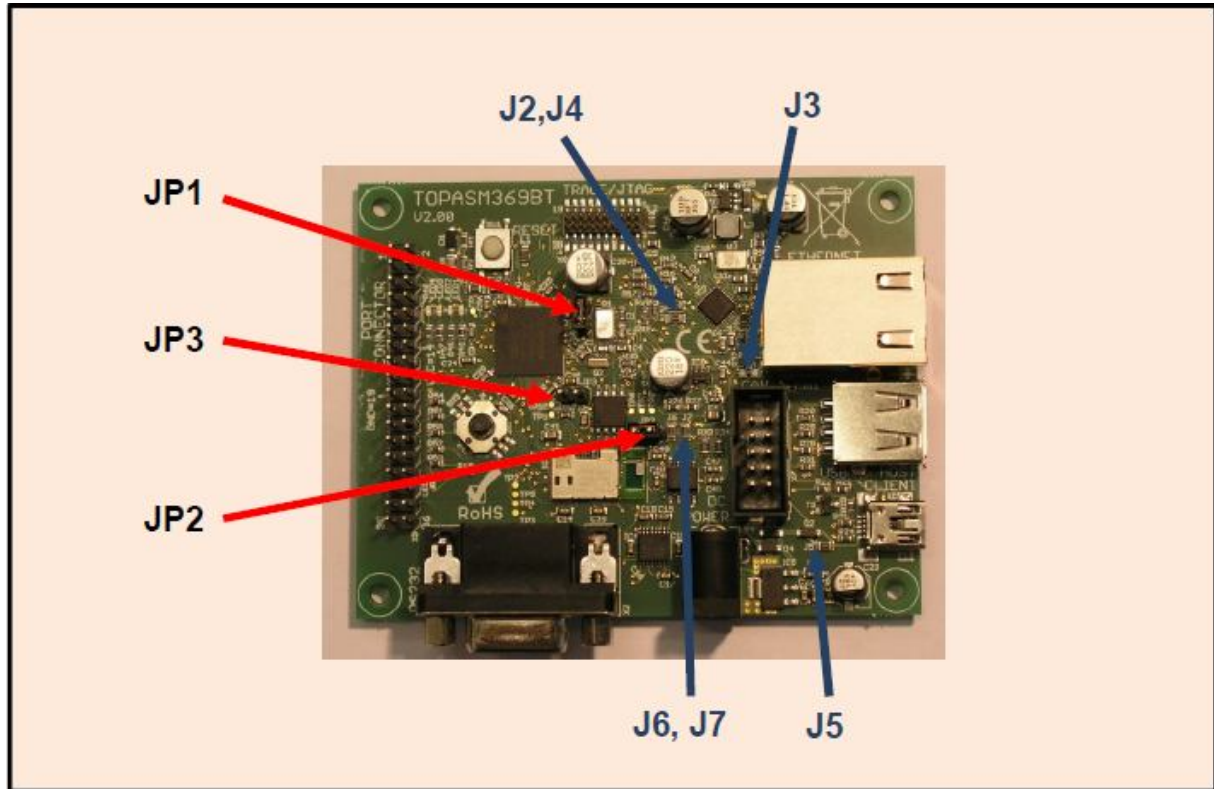
The +5V power of the USB host connector is switchable via port PK3 (active high enable) and has overcurrent detection connected to PK1 (active low trigger).

There are a few buttons and LEDs



- The joystick is connected to the port PD0...PD4.
- The user LEDs are connected to the port PC0...PC3.

There are some jumpers for power measurement and a jumper to enter the device operation mode named boot mode.



#### Jumpers JPx:

The jumpers JP1 and JP2 are normally closed. They can be opened to separate the microcontroller or the Bluetooth module from VCC and to insert an ammeter there in order to measure current draw.

- JP1 is for TMPM369.
- JP2 is for PAN1026.
- JP3 can be used to connect the BOOT-mode-pin of the microcontroller to GND.

#### Resistors Jx:

These 0-Ohm resistors can be easily identified because their SMD size is 0805 (regular resistors are SMD size 0603).

They are populated by default, except J3, and can be de-soldered to change the behavior of the board.

- J2 connects of PHY interrupt to MCU port PI3

- J3 connects LED of the PHY to MCU port PH7
- J4 connects reset input of the PHY to the reset button.
- J5 connects power input of the usb client connector to MCU port PK0
- J6 connects STB line of ZJA1040 CAN-Transceiver to GND.
- J7 connects pin 5 of CAN-connector to GND.

## 4. Step FOUR: Start the Sample Project

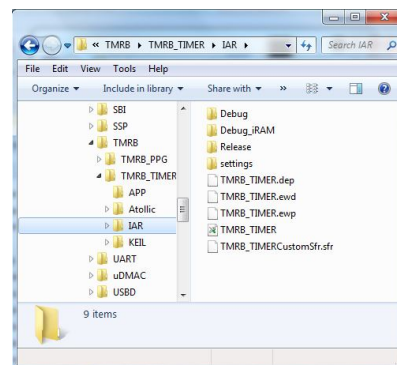
There are several software examples provided for the Microcontroller TMPM369. Hereby we show in a few steps how to start, compile and debug with an example that use the LED.

Please note that the project files are for the IAR® Tool chain. However, the sources could be used any tool environment.

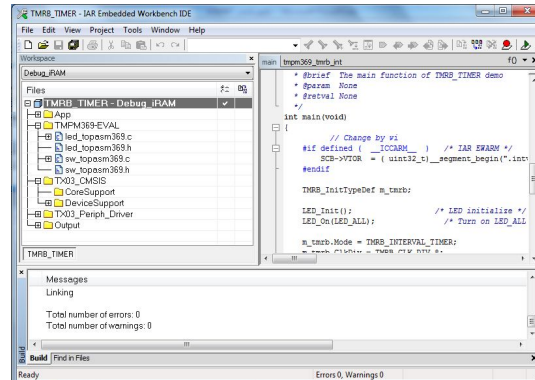
The software CMSIS driver, example and manual download:

[http://www.toshiba-components.com/microcontroller/Application\\_conf.html](http://www.toshiba-components.com/microcontroller/Application_conf.html)

1. Open a workspace from the folder by click on "TRMB\_TIMER.eww" or by the menu within the IDE.



2. Follow the instructions in the Example description to configure the board, and choose your build configuration, for example "Debug iRAM".
3. Choose Project>Make or click the button on the toolbar. The project should compile with no errors or warnings.
4. To download your program, choose Project>Download and Debug or click the button. The file main.c is now open in the editor window. Choose Debug>Go or click the button to start the application.
5. To stop C-SPY, choose Debug>Break or click the button.
6. To exit C-SPY, choose Debug>Stop Debugging or click the button.



## 5. Step FIVE: Start with Bluetooth

The Toshiba Bluetooth® LSI TC35661SBG has been embedded - including its SPP profile - into the Panasonic (PIDEU) module PAN1026. The embedded stack and profile were Bluetooth™ SIG approved through extensive BQB (Bluetooth™ Qualification Body) tests. The module is ideal for cable replacements. System developers can make use of the Serial Port Profile functionality through the comfortable Toshiba API interface over UART.

The Embedded SPP (Serial Port Profile) Application Note download:

<http://www.toshiba-components.com/bluetooth/index.html>

## 6. Pin Out of Connectors

### CAN Connector

	1	2	GND
CAN_L	3	4	CAN_H
GND	5	6	
	7	8	V+ (5V)
	9	10	

*Connector: CAN Signals according to CiA*

**Pin Header Connector**

TMPM369 has several ports. These ports are also used as input/output pins as well as for built-in peripheral functions. For the header pin some terminal has been selected. The description below shows the major function, for all please see TMPM369 Data Sheet.

PJ0 / AINB4	1	2	PJ1 / AINB5
PJ2 / AINB6	3	4	PJ3 / AINB7
PAN - GPIO05/FSYNC	5	6	GPIO16/DIN - PAN
PAN - GPIO17/CS0X	7	8	GPIO18/CS1X - PAN
DA0	9	10	DA1
PI0 / AIN0 / INT9	11	12	PI1 / AIN1 / INTA
PI2 / AIN2 / INTB	13	14	VCC
PE4 / SCLK1 / CTS1 / TB2OUT	15	16	PE5 / TXD1
PE6/ RXD1	17	18	PE7 / INT6 / TB2IN
GND	19	20	PF6 / DSR4 / SI1 /SCL1
PF7 / DTR4 / SO1/SDA1	21	22	PG0 / MT0IN
PG1	23	24	PG2 / MTOUT10 /MTTB0IN
PG3 / MTOUT00 / MTTB0OUT	25	26	PG4 / SP1CLK
PG5 / SP1DO	27	28	PG6
PG7 / SP1FSS	29	30	PB2 /SP2CLK / MTOUT03 /MTTB3OUT
PB3 / SP2DO /MTOUT13 /MTTB3IN	31	32	PB4 / SP2DI / INT7
PB5 / SP2FSS / MT3IN / INT1	33	34	PH1 / TB4OUT / SI2 / SCL2
PH0 / TB5OUT / MT2IN / SO2/SDA2	35	36	/NMI

*Connector: Pin Header*

*Note: BMSKTOPASM369BT(kc) version 1 has different pin header.*

## 7. Important Notice

- **Intended Usage**
  - The board should be used for evaluation. It should be used in proper environment. It is not intended and not recommended to be used in any final product.
- **Conformity**
  - The board passed the requirements of the radiated emission according to EN 61326-1 (CISPR 11) class A.
  - When using the boards in a Class B environment (residential and small commercial area) there is possibility to have increased radiation due to the LAN LSI oscillator frequency.
- **Known Limitations**
  - EEPROM: Please note that the schematics has been made using an EEPROM that use A2 terminal as "User Configurable Chip Select" and therefore it has been connected to logical 0 (GND). The 24LC1025 on the board use that terminal as block and not as chip select and it has to be connected to logical 1 (VCC). As a result only lower block, 512K bit, can be used for evaluation.

## 8. Revisions

Revision	Date	Description
0.91	31.05.2013	Initial Version for Board V1
01	30.07.2014	Initial Version for Board V2
02	27.01.2014	Add chapter "1. Important Notice" and "Revisions". Update pictures and some contents revised.



## 9. Disclaimer

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